

Continental Electronics

FPGA DIGITAL HW DESIGN MANAGER

Continental Electronics Corporation is a leading provider of RF technologies and innovative solutions with over 75 years of proven performance. CEC has experienced significant recent growth and has established itself as the go-to supplier for a diversified product line supporting a wide spectrum of markets from commercial products, military customers, to space research laboratories.

OBJECTIVE

The manager will be responsible for FPGA Development projects and associated hardware development needed to support the requirements of Continental Electronics products. Additionally, maintenance or enhancement of existing CEC hardware may be required, based on new requirements, unavailable components, or adaptation for use on other projects.

ESSENTIAL FUNCTIONS

- Lead hardware development in support of the FPGA development group. Should be familiar with current FPGA designs in use in CEC products and collaborate with key resource, leveraging existing FPGA design work when possible.
- Lead hardware development in support of the FPGA development
- Work with others to develop system architecture diagrams to ensure adequate information is available when beginning the project
- Lead the team through the process of understanding the requirements, creating additional diagrams as needed to support the development cycle
- Manage FPGA development utilizing Xilinx products and development tools
- Maintenance or enhancement of existing CEC hardware may be required, based on new requirements, unavailable components, or adaptation for use on other projects
- Lead circuit card layout efforts and ensure that the hardware development team can learn the skills needed to work with the layout designer
- Encourage team members to ensure all schematic components are available for purchase, and work with procurement as needed to order long-lead items well in advance of need
- Ensures open communication with the production group during development and as needed through test and system implementation
- Mentor team members to create circuit cards consistent with CEC's methodology

REQUIRED EXPERIENCE

- Bachelor's Degree in Electrical or Electronics Engineering or related field required, Master's Degree preferred
- Strong understanding of FPGA development processes and associated hardware development
- Strong knowledge in Xilinx products

PHYSICAL AND MENTAL DEMANDS

The physical demands described here are representative of those that must be met by an employee to successfully perform the essential functions of this job.

While performing the duties of this job, the employee is regularly required to talk and hear. This position is very active and requires standing, bending, kneeling, stooping, crouching, and walking all day. The employee must frequently lift and/or move items over 50 pounds. Specific vision abilities required by this job include close vision, distance vision, color vision, peripheral vision, depth perception and ability to adjust focus. Must regularly type using computer keyboards.

WORK ENVIRONMENT

Reasonable accommodations may be provided in order to allow qualified individuals with disabilities to perform the essential duties and tasks.

PHYSICAL DEMANDS

The physical demands described here are representative of those that must be met by an employee to successfully perform the essential functions of this job. Reasonable accommodations may be made to enable individuals with disabilities to perform the essential functions. While performing the duties of this Job, the employee is regularly required to sit and talk or hear. The employee is occasionally required to stand; walk; use hands to finger, handle, or feel; reach with hands and arms and stoop, kneel, crouch, or crawl. The employee must occasionally lift and/or move up to 10 pounds. Specific vision abilities required by this job include close vision, color vision, peripheral vision and ability to adjust focus.

Equal Opportunity Employer: minority / female / disability / veteran